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Abstract—Noise shaping digital quantizers, most commonly digital delta-sigma ( $\Delta\Sigma$ ) modulators, are used in fractional- $N$  phase-locked loops (PLLs) to enable fractional frequency tuning.

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to implement analog- and time-to-digital converters with first-order quantization and mismatch noise-shaping. To implement a time-to-digital converter (TDC) with noise-shaping, we present a oscillator that is enabled during the measurement of an input, and then disabled in between measurements.

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### **Optimal Delta-Sigma Modulation Based Noise Shaping for**

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